

Inf rmati n Disclosure

Statement By Applicant

(Use Several Sheets if Necessary)

Atty Docket No. ALTRP116/A1364

Applicant: Pritchard et al.

Filing Date

February 9, 2004

Application No.:

10/775,966

Group 2814 2828

## **U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A1						
-	A2						
	A3						
	A4						
	A5						

Foreign Patent or Published Foreign Patent Application

Examiner	Į	Document	Publication	Country or		Sub-	Trans	slation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	B1							
	B2							
	B3							
	B4							
	B5							

## **Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication		
٧S		Andrew DeHon, "Notes on Coupling Processors with Reconfigurable Logic", MIT Transit Project, Transit Note #118, downloaded from www.cs.caltech.eduresearch/ic/transit/tn118/tn118.html March 1995, 29 pages		
	C2			
	C3			
Examiner	VW	4E NEK Date Considered 7/6/06		

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.